

Notice of References Cited	Application/Control No. 10/078,065	Applicant(s)/Patent Under Reexamination WEHAGE, ERIC R.	
	Examiner John P Trimmings	Art Unit 2133	Page 1 of 1

U.S. PATENT DOCUMENTS

*		Document Number Country Code-Number-Kind Code	Date MM-YYYY	Name	Classification
	A	US-6,675,329	01-2004	Im, Jin Seok	714/718
	B	US-5,825,785	10-1998	Barry et al.	714/732
	C	US-6,425,103	07-2002	Phan, Tuan	714/733
	D	US-6,651,201	11-2003	Adams et al.	714/733
	E	US-6,760,865	07-2004	Ledford et al.	714/30
	F	US-6,415,403	07-2002	Huang et al.	714/726
	G	US-			
	H	US-			
	I	US-			
	J	US-			
	K	US-			
	L	US-			
	M	US-			

FOREIGN PATENT DOCUMENTS

*		Document Number Country Code-Number-Kind Code	Date MM-YYYY	Country	Name	Classification
	N					
	O					
	P					
	Q					
	R					
	S					
	T					

NON-PATENT DOCUMENTS

*		Include as applicable: Author, Title Date, Publisher, Edition or Volume, Pertinent Pages)
	U	V.D. Agrawal et al., "An Architecture for Synthesis of Testable Finite State Machines", 12-15 March 1990, EDAC Conference 1990, pp 612-616.
	V	
	W	
	X	

*A copy of this reference is not being furnished with this Office action. (See MPEP § 707.05(a).)
Dates in MM-YYYY format are publication dates. Classifications may be US or foreign.